

## FEATURES

- 10 $\mu$ s Short Circuit Withstand
- High Thermal Cycling Capability
- Soft Punch Through Silicon
- Isolated AlSiC Base with AlN Substrates
- Lead Free construction

## APPLICATIONS

- High Reliability Inverters
- Motor Controllers
- Traction Drives

The Powerline range of high power modules includes half bridge, chopper, dual, single and bi-directional switch configurations covering voltages from 600V to 6500V and currents up to 2400A.

The DIM250XCM65-TS000 is a 6500V, n-channel enhancement mode, insulated gate bipolar transistor (IGBT) chopper module. The IGBT has a wide reverse bias safe operating area (RBSOA) plus 10 $\mu$ s short circuit withstand. This device is optimised for traction drives and other applications requiring high thermal cycling capability.

The module incorporates an electrically isolated base plate and low inductance construction enabling circuit designers to optimise circuit layouts and utilise grounded heat sinks for safety.

## ORDERING INFORMATION

Order As:

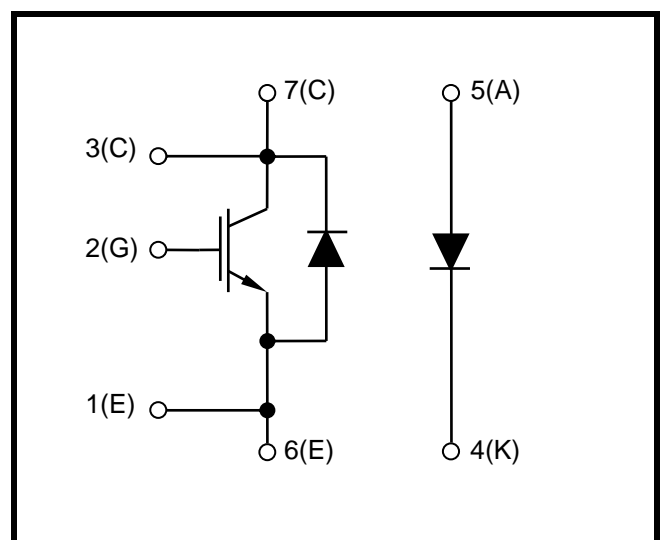
### **DIM250XCM65-TS000**

Note: When ordering, please use the complete part number

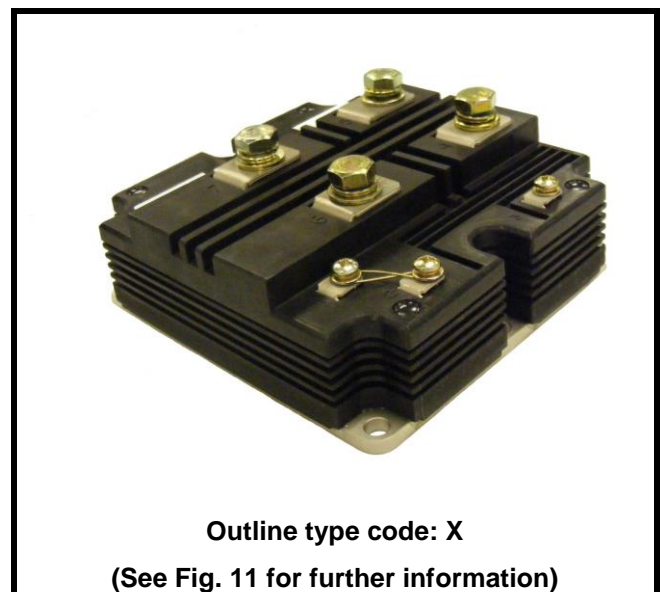
## KEY PARAMETERS

$V_{CES}$	<b>6500V</b>
$V_{CE(sat)}$ * (typ)	<b>3.0V</b>
$I_C$ (max)	<b>250A</b>
$I_{C(PK)}$ (max)	<b>500A</b>

\* Measured at the auxiliary terminals



**Fig. 1 Circuit configuration**



**Outline type code: X**  
**(See Fig. 11 for further information)**

**Fig. 2 Package**

## ABSOLUTE MAXIMUM RATINGS

Stresses above those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture of the package. Appropriate safety precautions should always be followed. Exposure to Absolute Maximum Ratings may affect device reliability.

$T_{case} = 25^{\circ}\text{C}$  unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
$V_{CES}$	Collector-emitter voltage	$V_{GE} = 0\text{V}, T_j = 125^{\circ}\text{C}$	6500	V
		$V_{GE} = 0\text{V}, T_j = 25^{\circ}\text{C}$	6500	V
		$V_{GE} = 0\text{V}, T_j = -40^{\circ}\text{C}$	6000	V
$V_{GES}$	Gate-emitter voltage		$\pm 20$	V
$I_C$	Continuous collector current	$T_{case} = 90^{\circ}\text{C}$	250	A
$I_{C(PK)}$	Peak collector current	1ms, $T_{case} = 115^{\circ}\text{C}$	500	A
$P_{max}$	Max. transistor power dissipation	$T_{case} = 25^{\circ}\text{C}, T_j = 125^{\circ}\text{C}$	3300	W
$I^2t$	Diode $I^2t$ value (IGBT arm)	$V_R = 0, t_p = 10\text{ms}, T_j = 125^{\circ}\text{C}$	22	$\text{kA}^2\text{s}$
	Diode $I^2t$ value (Diode arm)		22	$\text{kA}^2\text{s}$
$V_{isol}$	Isolation voltage – per module	Commoned terminals to base plate. AC RMS, 1 min, 50Hz	10.2	kV
$Q_{PD}$	Partial discharge – per module	IEC1287, $V_1 = 6900\text{V}, V_2 = 5100\text{V}, 50\text{Hz RMS}$	10	pC

## THERMAL AND MECHANICAL RATINGS

Internal insulation material:	AlN
Baseplate material:	AlSiC
Creepage distance:	56mm
Clearance:	26mm
CTI (Comparative Tracking Index):	>600

Symbol	Parameter	Test Conditions	Min	Typ.	Max	Units
$R_{th(j-c)}$	Thermal resistance – transistor (per arm)	Continuous dissipation – junction to case	-	-	30	$^{\circ}\text{C}/\text{kW}$
$R_{th(j-c)}$	Thermal resistance – diode (IGBT arm)	Continuous dissipation – junction to case			60	$^{\circ}\text{C}/\text{kW}$
	Thermal resistance – diode (Diode arm)				60	$^{\circ}\text{C}/\text{kW}$
$R_{th(c-h)}$	Thermal resistance – case to heatsink (per module)	Mounting torque 5Nm (with mounting grease)	-	-	8	$^{\circ}\text{C}/\text{kW}$
$T_j$	Junction temperature	Transistor	-	-	125	$^{\circ}\text{C}$
		Diode	-	-	125	$^{\circ}\text{C}$
$T_{stg}$	Storage temperature range	-	-40	-	125	$^{\circ}\text{C}$
	Screw torque	Mounting – M6	-	-	5	Nm
		Electrical connections – M4	-	-	2	Nm
		Electrical connections – M8	-	-	10	Nm

**ELECTRICAL CHARACTERISTICS**
**T<sub>case</sub> = 25°C unless stated otherwise.**

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
I <sub>CES</sub>	Collector cut-off current	V <sub>GE</sub> = 0V, V <sub>CE</sub> = V <sub>CES</sub>			1	mA
		V <sub>GE</sub> = 0V, V <sub>CE</sub> = V <sub>CES</sub> , T <sub>case</sub> = 125°C			30	mA
I <sub>GES</sub>	Gate leakage current	V <sub>GE</sub> = ± 20V, V <sub>CE</sub> = 0V			1	µA
V <sub>GE(TH)</sub>	Gate threshold voltage	I <sub>C</sub> = 40mA, V <sub>GE</sub> = V <sub>CE</sub>	5.5	6.5	7.5	V
V <sub>CE(sat)</sub> †	Collector-emitter saturation voltage	V <sub>GE</sub> = 15V, I <sub>C</sub> = 250A		3.0		V
		V <sub>GE</sub> = 15V, I <sub>C</sub> = 250A, T <sub>j</sub> = 125°C		4.0		V
I <sub>F</sub>	Diode forward current	DC			250	A
I <sub>FM</sub>	Diode maximum forward current	t <sub>p</sub> = 1ms			500	A
V <sub>F</sub> †	Diode forward voltage (IGBT arm)	I <sub>F</sub> = 250A		3.6		V
	Diode forward voltage (Diode arm)			3.6		V
	Diode forward voltage (IGBT arm)	I <sub>F</sub> = 250A, T <sub>j</sub> = 125°C		4.3		V
	Diode forward voltage (Diode arm)			4.3		V
C <sub>ies</sub>	Input capacitance	V <sub>CE</sub> = 25V, V <sub>GE</sub> = 0V, f = 1MHz		40		nF
Q <sub>g</sub>	Gate charge	±15V		3		µC
C <sub>res</sub>	Reverse transfer capacitance	V <sub>CE</sub> = 25V, V <sub>GE</sub> = 0V, f = 1MHz		0.8		nF
L <sub>M</sub>	Module inductance – per arm			25		nH
R <sub>INT</sub>	Internal resistance – per arm			270		µΩ
SC <sub>Data</sub>	Short circuit current, I <sub>SC</sub>	T <sub>j</sub> = 125°C, V <sub>CC</sub> = 4400V t <sub>p</sub> ≤ 10µs, V <sub>GE</sub> ≤ 15V V <sub>CE(max)</sub> = V <sub>CES</sub> – L* x di/dt IEC 60747-9		1200		A

**Note:**

† Measured at the auxiliary terminals

\* L is the circuit inductance + L<sub>M</sub>

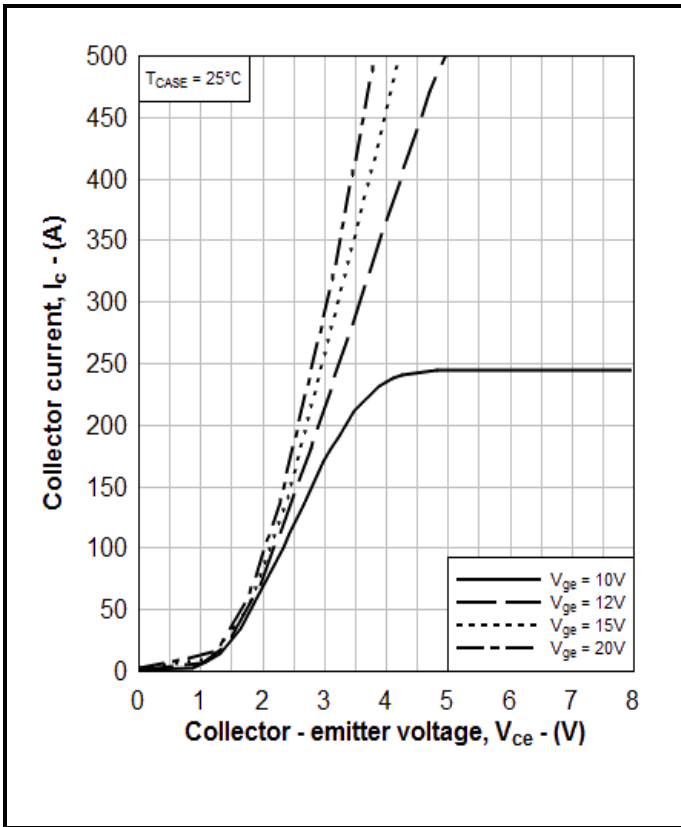
## ELECTRICAL CHARACTERISTICS

$T_{case} = 25^{\circ}\text{C}$  unless stated otherwise

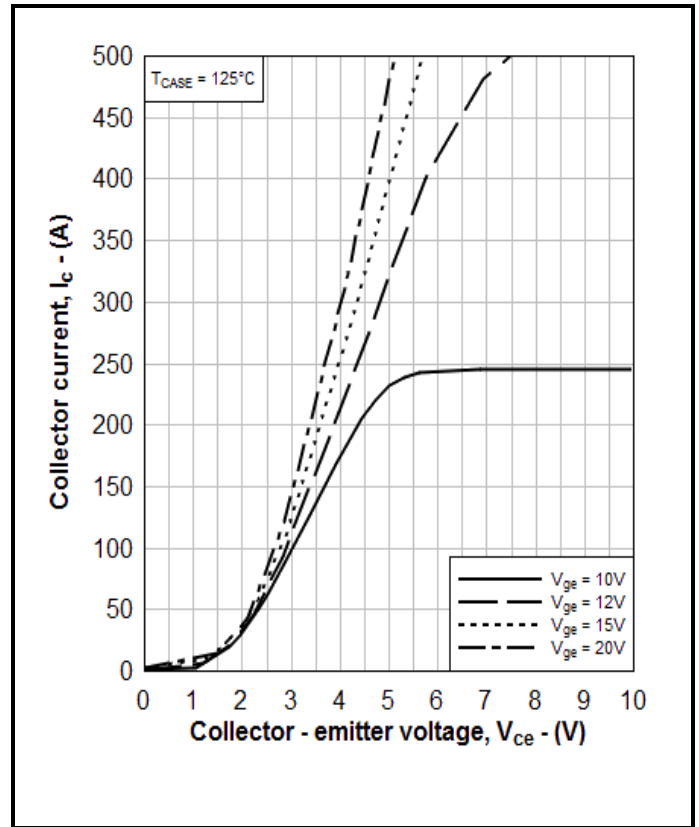
Symbol	Parameter	Test Conditions	Min	Typ.	Max	Units
$t_{d(off)}$	Turn-off delay time	$I_C = 250\text{A}$ $V_{GE} = \pm 15\text{V}$ $V_{CE} = 3600\text{V}$ $R_{G(ON)} = 6.8\Omega$ $R_{G(OFF)} = 22\Omega$ $C_{ge} = 100\text{nF}$ $L_S \sim 280\text{nH}$		3.6		$\mu\text{s}$
$t_f$	Fall time			450		ns
$E_{OFF}$	Turn-off energy loss			1300		mJ
$t_{d(on)}$	Turn-on delay time			900		ns
$t_r$	Rise time			400		ns
$E_{ON}$	Turn-on energy loss			1600		mJ
$Q_{rr}$	Diode reverse recovery charge	<b>Diode arm</b>		400		$\mu\text{C}$
$I_{rr}$	Diode reverse recovery current	$I_F = 250\text{A}$		300		A
$E_{rec}$	Diode reverse recovery energy	$V_{CE} = 3600\text{V}$ $dI_F/dt = 700\text{A}/\mu\text{s}$		830		mJ

$T_{case} = 125^{\circ}\text{C}$  unless stated otherwise

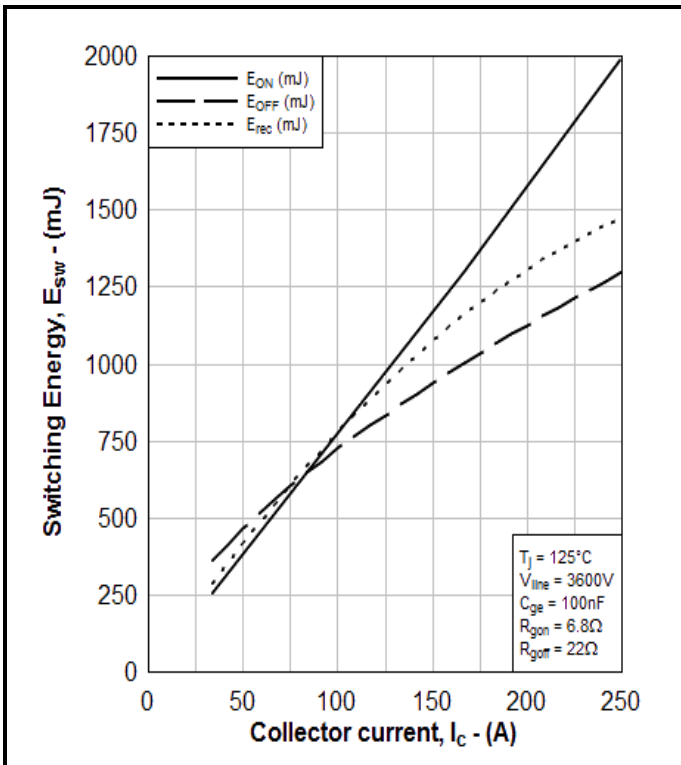
Symbol	Parameter	Test Conditions	Min	Typ.	Max	Units
$t_{d(off)}$	Turn-off delay time	$I_C = 250\text{A}$ $V_{GE} = \pm 15\text{V}$ $V_{CE} = 3600\text{V}$ $R_{G(ON)} = 6.8\Omega$ $R_{G(OFF)} = 22\Omega$ $C_{ge} = 100\text{nF}$ $L_S \sim 280\text{nH}$		3.6		$\mu\text{s}$
$t_f$	Fall time			450		ns
$E_{OFF}$	Turn-off energy loss			1350		mJ
$t_{d(on)}$	Turn-on delay time			800		ns
$t_r$	Rise time			450		ns
$E_{ON}$	Turn-on energy loss			2000		mJ
$Q_{rr}$	Diode reverse recovery charge	<b>Diode arm</b>		700		$\mu\text{C}$
$I_{rr}$	Diode reverse recovery current	$I_F = 250\text{A}$		340		A
$E_{rec}$	Diode reverse recovery energy	$V_{CE} = 3600\text{V}$ $dI_F/dt = 700\text{A}/\mu\text{s}$		1500		mJ



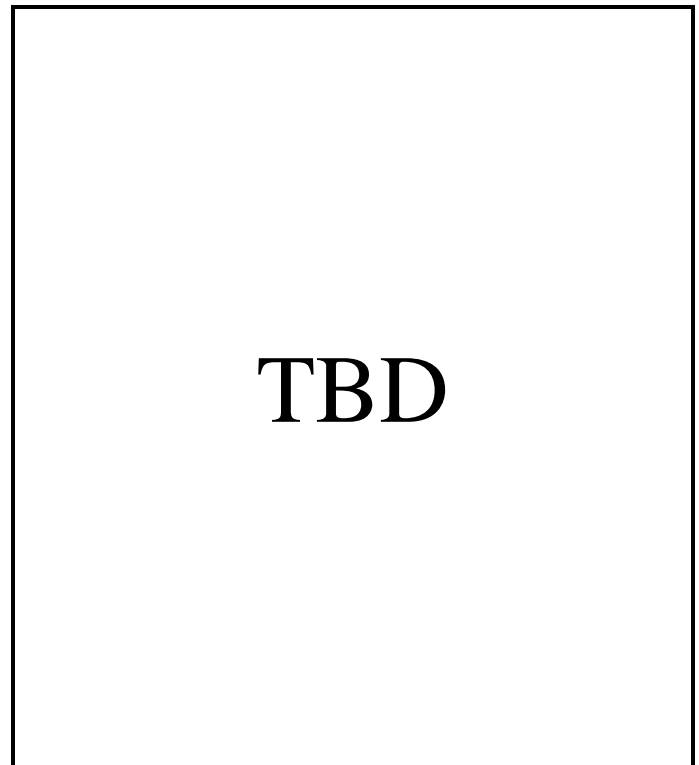
**Fig. 3 Typical output characteristics**



**Fig. 4 Typical output characteristics**



**Fig. 5 Typical switching energy vs collector current**



**Fig. 6 Typical switching energy vs gate resistance**

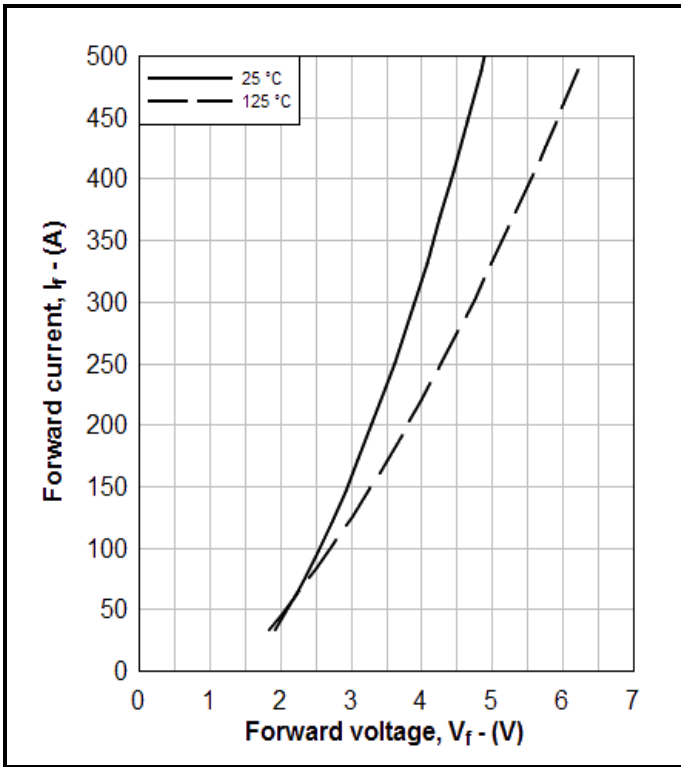


Fig. 7 Diode typical forward characteristics

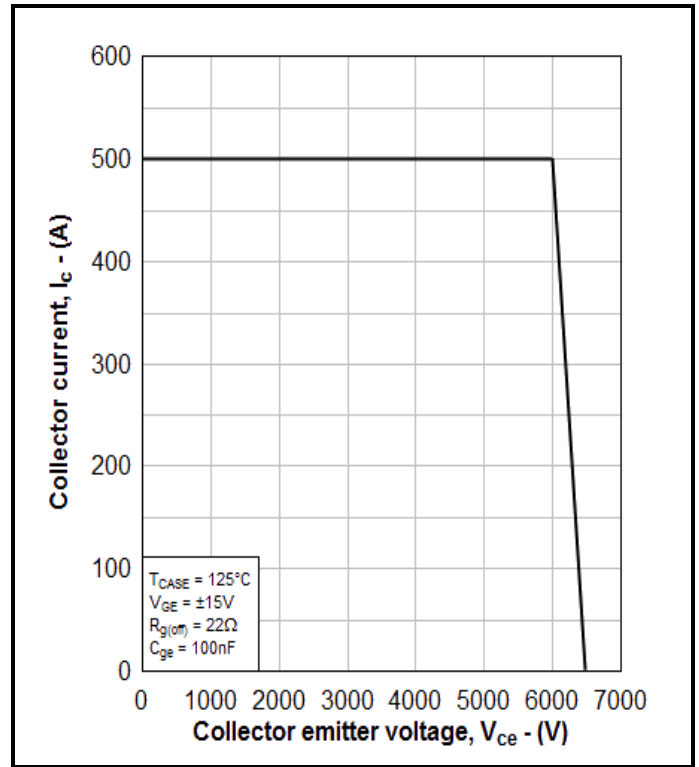


Fig. 8 Reverse bias safe operating area

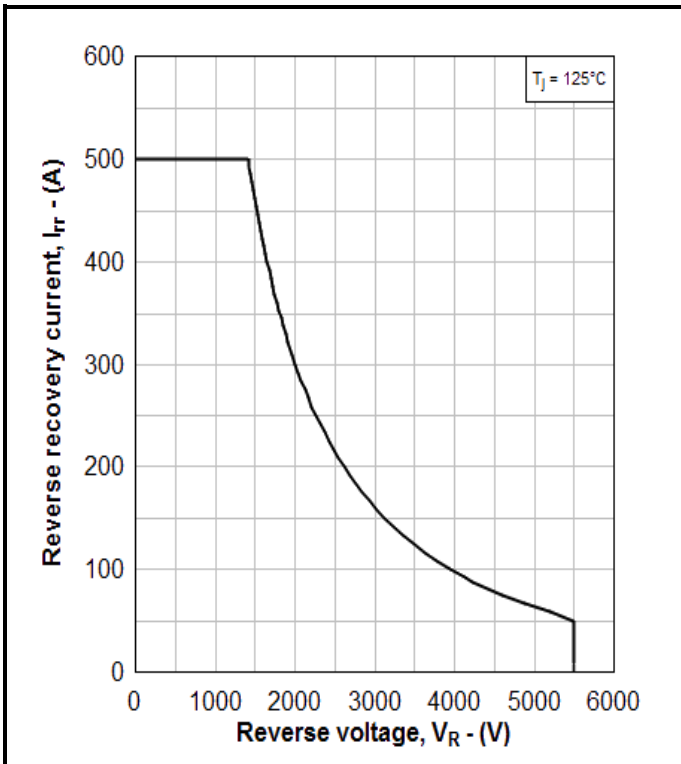


Fig. 9 Diode reverse bias safe operating area

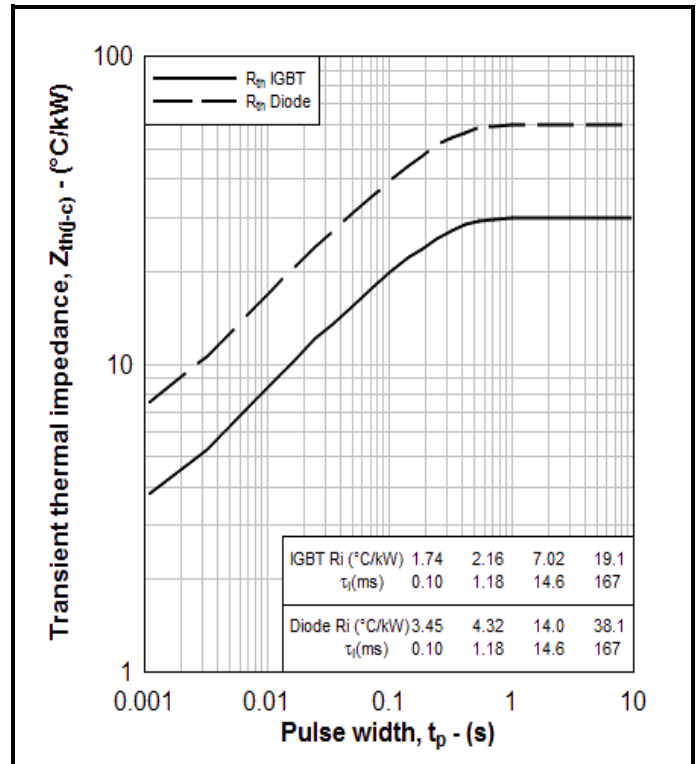
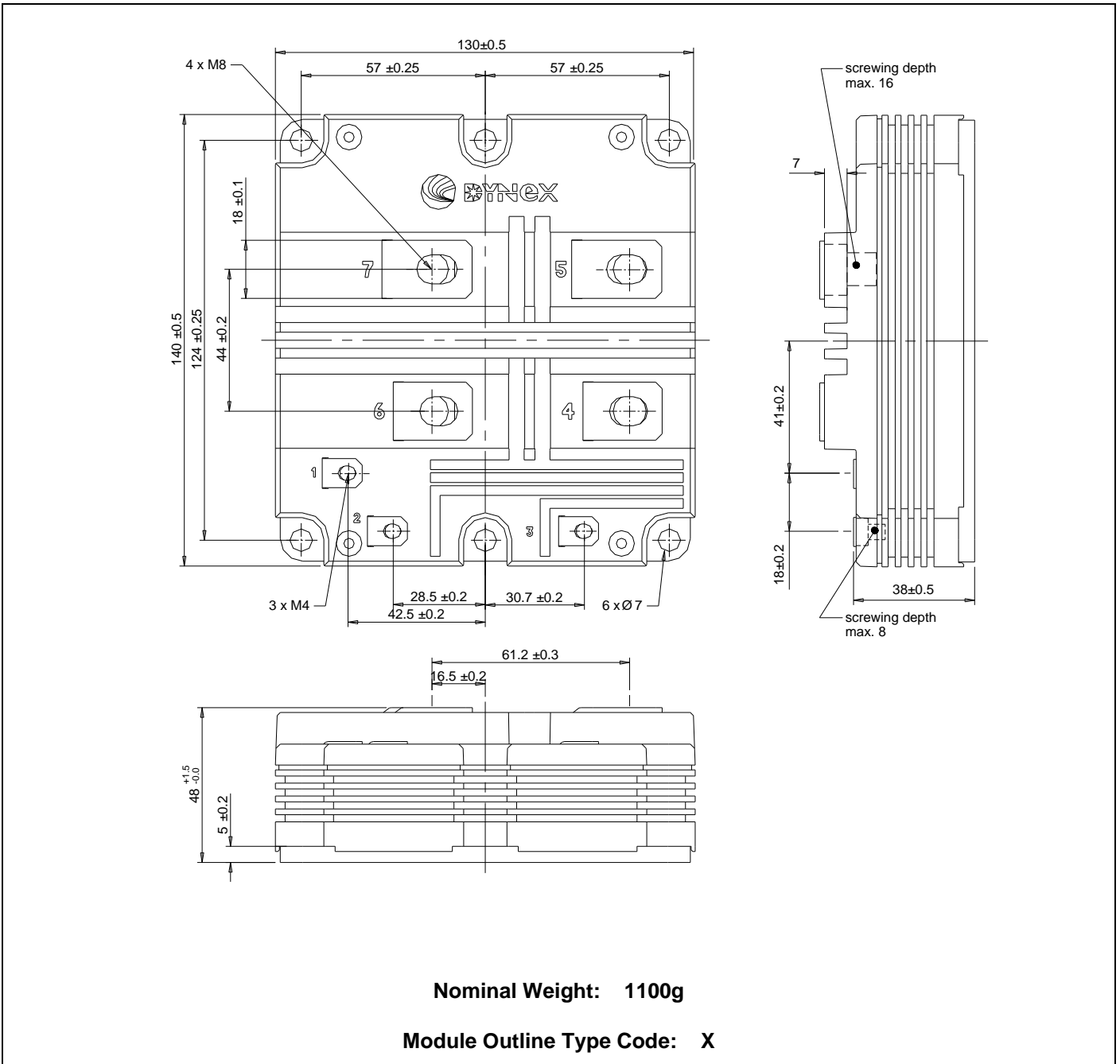


Fig. 10 Transient thermal impedance

**PACKAGE DETAILS**

For further package information, please visit our website or contact Customer Services.  
 All dimensions in mm, unless stated otherwise.  
**DO NOT SCALE.**



**Nominal Weight: 1100g**

**Module Outline Type Code: X**

**Fig. 11 Module outline drawing**

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## HEADQUARTERS OPERATIONS

### DYNEX SEMICONDUCTOR LTD

Doddington Road, Lincoln, Lincolnshire, LN6 3LF,  
United Kingdom

Fax: +44(0)1522 500550

Tel: +44(0)1522 500500

Web: <http://www.dynexsemi.com>

## CUSTOMER SERVICE

### DYNEX SEMICONDUCTOR LTD

Doddington Road, Lincoln, Lincolnshire, LN6 3LF,  
United Kingdom

Fax: +44(0)1522 500020

Tel: +44(0)1522 502753 / 502901

Email: [Power\\_solutions@dynexsemi.com](mailto:Power_solutions@dynexsemi.com)